E

JAN 0 2 2004 W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Applicant:	Shin Hwa Li	Docket No :	98-P-009C2
Serial No:	09/632,388	Group No:	2823
Filed:	August 3, 2000	Examiner :	Julio J. Maldonado
For:	Semiconductor Structure Having An Improved Pre-Metal Dielectric Stack And Method For Forming The Same	Confirmation No :	1164

TRANSMITTAL LETTER

Mail Stop Issue Fee Attention: Official Draftsman Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability (PTO-37) mailed October 1, 2003, for the above-identified patent application, please find enclosed for filing two (2) sheets of formal drawings.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted

Mario J. Donato, Jr. Reg. No. 37,816 Attorney for Applicant

STMicroelectronics, Inc. 1310 Electronics Drive/MS 2346 Carrollton, TX 75006 972-466-7503

CERTIFICATE OF MAILING 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage, in an enveloped addressed to: Mail Stop Issue Fee Attention: Official Draftsman Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-14501 on the date below:

December 29, 2003

Date

Signature